



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gregory E. Howard, et al. Docket No: TI-29600.1
Serial No: 10/719,198 Conf. No: 2992
Examiner: Shrinivas H. Rao Art Unit: 2814
Filed: 11/21/2003
For: METHOD FOR CONSTRUCTING A METAL OXIDE SEMICONDUCTOR FIELD EFFECT TRANSISTOR

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 11-17-04.

Ann Trent
Ann Trent

Dear Sir:

Responsive to the Office Action mailed July 29, 2004, in connection with the above identified application, Applicants respectfully submit the following remarks.